

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization International Bureau



(43) International Publication Date  
16 June 2005 (16.06.2005)

PCT

(10) International Publication Number  
WO 2005/055226 A1

(51) International Patent Classification<sup>7</sup>: G11B 19/02, (20/10, G06F 1/32, 3/06)

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(21) International Application Number: PCT/IB2004/052506

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(22) International Filing Date: 22 November 2004 (22.11.2004)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data: 03104522.2 3 December 2003 (03.12.2003) EP

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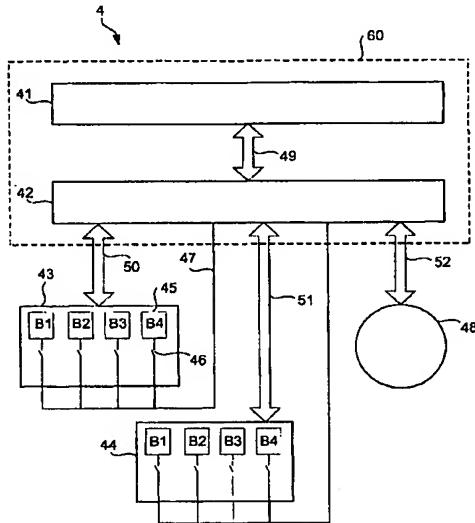
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(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SI, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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(54) Title: POWER SAVING METHOD AND SYSTEM



(57) **Abstract:** The present invention relates to reduction of power consumption of electronic mass storage devices, and more particularly to such a reduction of power consumption in mobile infotainment products. These devices are equipped with a subsystem comprising a mass storage device (48) and a buffer memory (43, 44). The size of the buffer memory (43, 44) is adapted in such a way that optimally low power consumption is achieved. This accomplishment by activating or deactivating memory banks (45) comprised in the buffer memory chips. The amount of memory banks (45) activated is determined by operating characteristics of the subsystem, e.g. a desired bit-rate to be achieved for transmissions to/from the mass storage device (48).

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